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WHAT IS CLAIMED IS:

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1.	\mathbf{A}	semiconductor	GE ATCE	COMPT TO TITE

- a supporting substrate made of insulating material;
- a conductive pattern provided on a surface of the supporting substrate;

an external connecting terminal provided on a back surface of the supporting substrate and electrically connected to the conductive patterns;

a circuit element provided on the conductive pattern;

a glass plate that covers the circuit element and that forms a hollow airtight portion between the supporting substrate and the glass plate; and

an adhesive resin applied to an overall adhered surface of the glass plate.

2. A semiconductor device according to claim 1, wherein the adhesive resin is formed of a light-shielding adhesive resin.

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- 3. A semiconductor device according to claim 1, wherein the circuit element includes a semiconductor element or a fuse element.
- 25 4. A semiconductor device manufacturing method

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comprising steps of:

preparing a supporting substrate in which a conductive pattern having a large number of mounting portions on a surface of the supporting substrate is provided and external connecting terminals are provided on a back surface;

fixing the circuit element onto the mounting portions respectively;

applying an adhesive resin to an overall adhesive surface of a glass plate that covers the circuit element and forms an airtight hollow portion between the supporting substrate and the glass plate every mounting portion;

adhering the supporting substrate and the glass plate to form the airtight hollow portion every mounting portion; and

dividing the supporting substrate into respective mounting portions by dicing adhered portions between the supporting substrate and the glass plate.

- 5. A semiconductor device manufacturing method according to claim 4, wherein the adhesive resin is formed of a light-shielding adhesive resin.
 - 6. A semiconductor device manufacturing method comprising steps of:
- 25 preparing a supporting substrate in which conductive

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patterns having a number of mounting portions thereon are provided on a surface of the supporting substrate and external connecting terminals are provided on a back surface of the supporting substrate;

fixing a circuit element onto respective mounting portions;

mounting a lattice-like column member on the supporting substrate;

applying an adhesive resin to an overall adhesive surface of a glass plate that covers the circuit element and forms an airtight hollow portion between the supporting substrate and the glass plate every mounting portion;

adhering a glass plate onto the column member to cover the circuit element and to form a hollow airtight portion formed by the supporting substrate, the column member and the glass plate every mounting portion; and

dividing the supporting substrate into respective mounting portions by dicing adhered portions between the supporting substrate and the glass plate.

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7. A semiconductor device manufacturing method according to claim 6, wherein the adhesive resin is formed of a light-shielding adhesive resin.